IN THE SPECIFICATION

On page 3, last paragraph, replace as follows:

FIGURE 6a is an electrical schematic of a multiplexed display system.

FIGURE 6b is a schematic diagram of multiplexed indicators and data tap scheme for use with the present system.

On page 8, first paragraph:

An example, of a multiplexed system can be seen in FIGURE 6a. Twenty-four (24) LED's LED1-LED24, are organized into a three row by eight column matrix. As demonstrated in the multiplexer timing diagram in FIGURE 6b, each row of eight LED's is selected through a common switching transistor, Q9 – Q11 respectively, by applying a drive pulse to the base, terminal two of the device. The individual LED's within a common row are selected in parallel by applying a drive signal to the base of the column driver transistor Q1 – Q8 respectively. In order to turn on LED10, it is necessary to apply drive signals to Q2 at the time Q10 is driven [C1 x ROW 1] at time T2. Referring again to FIGURE 6b, to [[To]] capture the data column pattern for each row, it will be necessary that the C0 – C7 and ROW0 – ROW2 signals pass from the data tap 8 to the translator 6 for processing.